



501.37370CC4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: N. OHASHI, et al.
Application No.: 10/760,292
Filed: January 21, 2004
For: PROCESS FOR MANUFACTURING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Group AU: 2812
Examiner: Ha T. Nguyen
Confirm. No.: 9846

AMENDMENT

Mail Stop: Amendment - Fee

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 22, 2005

Sir:

In response to the Office Action mailed April 22, 2005, please amend the above-identified application as listed in the following, and as set forth on the following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.

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